



SIMATIC DP, Electronics module 2 AO U High Feature for ET 200S, 15 mm width, Cycle time per module: 0.5 ms, +/-10V; 15 bit+sign, 1.. 5V; 15 bit, Operational limit +/-0.07% with SF LED (group fault)

General information	
Product function	
<ul style="list-style-type: none"> • Isochronous mode 	Yes
Supply voltage	
Load voltage L+	
<ul style="list-style-type: none"> • Rated value (DC) 	24 V
<ul style="list-style-type: none"> • Reverse polarity protection 	Yes
Input current	
from load voltage L+ (without load), max.	80 mA
from backplane bus 3.3 V DC, max.	10 mA
Power loss	
Power loss, max.	1.2 W
Address area	
Address space per module	
<ul style="list-style-type: none"> • Address space per module, max. 	4 byte
Analog outputs	
Number of analog outputs	2
Voltage output, short-circuit protection	Yes
Voltage output, short-circuit current, max.	25 mA
Cycle time (all channels) max.	0.5 ms; At max. 0.5 μ F
Output ranges, voltage	
<ul style="list-style-type: none"> • 1 V to 5 V 	Yes
<ul style="list-style-type: none"> • -5 V to +5 V 	Yes
<ul style="list-style-type: none"> • -10 V to +10 V 	Yes
Connection of actuators	
<ul style="list-style-type: none"> • for voltage output two-wire connection 	Yes
<ul style="list-style-type: none"> • for voltage output four-wire connection 	Yes
Load impedance (in rated range of output)	
<ul style="list-style-type: none"> • with voltage outputs, min. 	1 k Ω
<ul style="list-style-type: none"> • with voltage outputs, capacitive load, max. 	0.5 μ F
Destruction limits against externally applied voltages and currents	
<ul style="list-style-type: none"> • Voltages at the outputs towards MANA 	15 V; max. 15 V continuous; 75 V for max. 1 s (mark to space ratio 1:20)
Cable length	
<ul style="list-style-type: none"> • shielded, max. 	200 m; 100 m if Twa < 2 ms
Analog value generation for the outputs	
Integration and conversion time/resolution per channel	
<ul style="list-style-type: none"> • Resolution with overrange (bit including sign), max. 	16 bit; 15 bit + sign

Settling time	
<ul style="list-style-type: none"> • for resistive load • for capacitive load • for inductive load 	0.2 ms 0.5 ms; At max. 0.5 µF 0.5 ms
Errors/accuracies	
Operational error limit in overall temperature range	
<ul style="list-style-type: none"> • Voltage, relative to output range, (+/-) 	0.1 %
Basic error limit (operational limit at 25 °C)	
<ul style="list-style-type: none"> • Voltage, relative to output range, (+/-) 	0.05 %
Interrupts/diagnostics/status information	
Diagnoses	
<ul style="list-style-type: none"> • Diagnostic information readable • Short-circuit • Group error 	Yes Yes Yes
Diagnostics indication LED	
<ul style="list-style-type: none"> • Group error SF (red) 	Yes
Parameter	
Remark	7 byte
Diagnostics short-circuit	Disable / enable
Group diagnostics	Disable / enable
Response to CPU/master STOP	Output current and de-energized/substitute a value/keep last value
Potential separation	
Potential separation analog outputs	
<ul style="list-style-type: none"> • between the channels • between the channels and backplane bus • Between the channels and load voltage L+ 	No Yes Yes
Isolation	
Isolation tested with	500 V DC
Dimensions	
Width	15 mm
Height	81 mm
Depth	52 mm
Weights	
Weight, approx.	40 g
last modified:	1/16/2021 