## **SIEMENS**

## **Data sheet**

6ES7677-2SB42-0GL0



Figure similar

SIMATIC ET 200SP Open Controller, CPU 1515SP PC2 F + HMI 512PT, 8 GB RAM, 128 GB CFast with Windows 10 IoT Enterprise 64-bit, S7-1500 Failsafe Software Controller CPU 1505SP F and WinCC Runtime Advanced pre-installed, with 512 PowerTags license; Interfaces: 1x Slot CFast, 1x slot SD/MMC, 1x connection for ET 200SP bus Adapter PROFINET, 1x 10/100/1000 Mbit/s Ethernet, 2x USB 3.0, 2x USB 2.0, 1x display port, Documentation on CFast Restore image on CFast

| General information  |  |
|--|--|
| Product type designation                                     | CPU 1515SP PC2 F + HMI 512                                       |
| HW functional status   | from FS04  |
| Firmware version   | V20.8  |
| Engineering with   |  |
| STEP 7 TIA Portal configurable/integrated from version       | V16  |
| Installed software   |  |
| <ul><li>Visualization</li></ul>                              | WinCC Runtime Advanced V16                                       |
| Control  | S7-1500 Software Controller CPU 1505SP F                         |
| Configuration control  |  |
| via dataset  | Yes  |
| Control elements   |  |
| Mode selector switch   | 1  |
| Supply voltage   |  |
| Type of supply voltage                                       | 24 V DC  |
| permissible range, lower limit (DC)                          | 19.2 V   |
| permissible range, upper limit (DC)                          | 28.8 V   |
| Reverse polarity protection                                  | Yes  |
| Mains buffering  |  |
| <ul> <li>Mains/voltage failure stored energy time</li> </ul> | 5 ms   |
| Input current  |  |
| Current consumption (rated value)                            | 1.8 A; Full processor load, incl. ET 200SP modules and using USB |
| Current consumption (in no-load operation), typ.             | 0.5 A  |
| Current consumption, max.                                    | 2.9 A  |
| I <sup>2</sup> t   | 0.426 A <sup>2</sup> ·s; with starting current inrush            |
| Power  |  |
| Active power input, max.                                     | 55 W; incl. ET 200SP modules and using USB                       |
| Infeed power to the backplane bus                            | 8.75 W   |
| Power loss   |  |
| Power loss, typ.   | 16 W   |
| Processor  |  |
| Processor type   | Intel Atom E3940, 1.6 GHz, 4 cores                               |
| Memory   |  |
| Type of memory   | DDR3L  |
| Main memory  | 8 GB RAM   |

| CFast memory card   | Yes; 128 GB flash memory   |
|---|--|
| SIMATIC memory card required  | No   |
| Work memory   |  |
| • integrated (for program)  | 1.5 Mbyte  |
| • integrated (for data)   | 5 Mbyte  |
| • integrated (for CPU function library of CPU   | 20 Mbyte   |
| Runtime)  | · ·  |
| Load memory   |  |
| integrated (on PC mass storage)   | 320 Mbyte  |
| Backup  |  |
| • with UPS  | Yes; all memory areas declared retentive   |
| with non-volatile memory  | Yes  |
| CPU processing times  |  |
| for bit operations, typ.  | 10 ns  |
| for word operations, typ.   | 12 ns  |
| for fixed point arithmetic, typ.  | 16 ns  |
| for floating point arithmetic, typ.   | 64 ns  |
| CPU-blocks  |  |
| Number of elements (total)  | 6 000; In addition to blocks such as DBs, FBs and FCs, UDTs, global  |
|   | constants, etc. are also regarded as elements  |
| DB  |  |
| Number, max.  | 5 999; Number range: 1 to 65535  |
| Size, max.  | 5 Mbyte  |
| FB  |  |
| <ul><li>Number, max.</li></ul>  | 5 998; Number range: 1 to 65535  |
| • Size, max.  | 1 024 kbyte  |
| FC  |  |
| Number, max.  | 5 999; Number range: 1 to 65535  |
| • Size, max.  | 1 024 kbyte  |
| OB  | 4.004 lb. 4-   |
| • Size, max.  | 1 024 kbyte  |
| Number of free cycle OBs  | 100  |
| <ul> <li>Number of time alarm OBs</li> </ul>  | 20   |
| November of delevirolence OD-   | 20   |
| Number of delay alarm OBs     Number of qualic intermet OBs   | 20   |
| Number of cyclic interrupt OBs  | 20   |
| <ul><li>Number of cyclic interrupt OBs</li><li>Number of process alarm OBs</li></ul>  | 50   |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> </ul>   | 50<br>3  |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> </ul>   | 50<br>3<br>1   |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> </ul>   | 50<br>3<br>1<br>2  |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> <li>Number of startup OBs</li> </ul>  | 50<br>3<br>1<br>2<br>100   |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> <li>Number of startup OBs</li> <li>Number of asynchronous error OBs</li> </ul>  | 50<br>3<br>1<br>2<br>100<br>4  |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> <li>Number of startup OBs</li> <li>Number of asynchronous error OBs</li> <li>Number of synchronous error OBs</li> </ul>   | 50<br>3<br>1<br>2<br>100<br>4<br>2   |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> <li>Number of startup OBs</li> <li>Number of asynchronous error OBs</li> <li>Number of synchronous error OBs</li> <li>Number of diagnostic alarm OBs</li> </ul>   | 50<br>3<br>1<br>2<br>100<br>4  |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> <li>Number of startup OBs</li> <li>Number of asynchronous error OBs</li> <li>Number of synchronous error OBs</li> <li>Number of diagnostic alarm OBs</li> </ul>   | 50<br>3<br>1<br>2<br>100<br>4<br>2   |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> <li>Number of startup OBs</li> <li>Number of asynchronous error OBs</li> <li>Number of synchronous error OBs</li> <li>Number of diagnostic alarm OBs</li> </ul> Nesting depth <ul> <li>per priority class</li> </ul>  | 50<br>3<br>1<br>2<br>100<br>4<br>2   |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> <li>Number of startup OBs</li> <li>Number of asynchronous error OBs</li> <li>Number of synchronous error OBs</li> <li>Number of diagnostic alarm OBs</li> </ul> Nesting depth <ul> <li>per priority class</li> </ul> Counters, timers and their retentivity   | 50<br>3<br>1<br>2<br>100<br>4<br>2   |
| Number of cyclic interrupt OBs  Number of process alarm OBs  Number of DPV1 alarm OBs  Number of isochronous mode OBs  Number of technology synchronous alarm OBs  Number of startup OBs  Number of asynchronous error OBs  Number of synchronous error OBs  Number of diagnostic alarm OBs  Nesting depth  per priority class  Counters, timers and their retentivity  S7 counter  | 50<br>3<br>1<br>2<br>100<br>4<br>2<br>1<br>24; Up to 8 possible for F-blocks                                 |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> <li>Number of startup OBs</li> <li>Number of asynchronous error OBs</li> <li>Number of synchronous error OBs</li> <li>Number of diagnostic alarm OBs</li> </ul> Nesting depth <ul> <li>per priority class</li> </ul> Counters, timers and their retentivity S7 counter <ul> <li>Number</li> </ul>   | 50<br>3<br>1<br>2<br>100<br>4<br>2   |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> <li>Number of startup OBs</li> <li>Number of asynchronous error OBs</li> <li>Number of synchronous error OBs</li> <li>Number of diagnostic alarm OBs</li> <li>Nesting depth</li> <li>per priority class</li> <li>Counters, timers and their retentivity</li> <li>S7 counter</li> <li>Number</li> <li>Retentivity</li> </ul>   | 50<br>3<br>1<br>2<br>100<br>4<br>2<br>1<br>24; Up to 8 possible for F-blocks                                 |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> <li>Number of startup OBs</li> <li>Number of asynchronous error OBs</li> <li>Number of synchronous error OBs</li> <li>Number of diagnostic alarm OBs</li> </ul> Nesting depth <ul> <li>per priority class</li> </ul> Counters, timers and their retentivity S7 counter <ul> <li>Number</li> <li>Retentivity</li> <li>adjustable</li> </ul>  | 50<br>3<br>1<br>2<br>100<br>4<br>2<br>1<br>24; Up to 8 possible for F-blocks                                 |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> <li>Number of startup OBs</li> <li>Number of asynchronous error OBs</li> <li>Number of synchronous error OBs</li> <li>Number of diagnostic alarm OBs</li> </ul> Nesting depth <ul> <li>per priority class</li> </ul> Counters, timers and their retentivity S7 counter <ul> <li>Number</li> <li>Retentivity</li> <li>adjustable</li> </ul> IEC counter IEC counter  | 50 3 1 2 100 4 2 1 24; Up to 8 possible for F-blocks  2 048  Yes   |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> <li>Number of startup OBs</li> <li>Number of asynchronous error OBs</li> <li>Number of synchronous error OBs</li> <li>Number of diagnostic alarm OBs</li> <li>Nesting depth <ul> <li>per priority class</li> </ul> </li> <li>Counters, timers and their retentivity</li> <li>S7 counter <ul> <li>Number</li> <li>Retentivity</li> <li>adjustable</li> </ul> </li> <li>IEC counter</li> <li>Number</li> </ul>  | 50<br>3<br>1<br>2<br>100<br>4<br>2<br>1<br>24; Up to 8 possible for F-blocks                                 |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> <li>Number of startup OBs</li> <li>Number of asynchronous error OBs</li> <li>Number of synchronous error OBs</li> <li>Number of diagnostic alarm OBs</li> </ul> Nesting depth <ul> <li>per priority class</li> </ul> Counters, timers and their retentivity S7 counter <ul> <li>Number</li> <li>Retentivity</li> <li>adjustable</li> </ul> IEC counter <ul> <li>Number</li> <li>Retentivity</li> </ul>  | 50 3 1 2 100 4 2 1 1 24; Up to 8 possible for F-blocks  2 048  Yes  Any (only limited by the main memory)    |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> <li>Number of startup OBs</li> <li>Number of asynchronous error OBs</li> <li>Number of synchronous error OBs</li> <li>Number of diagnostic alarm OBs</li> <li>Nesting depth</li> <li>per priority class</li> <li>Counters, timers and their retentivity</li> <li>S7 counter</li> <li>Number</li> <li>Retentivity</li> <li>— adjustable</li> <li>IEC counter</li> <li>Number</li> <li>Retentivity</li> <li>— adjustable</li> </ul>                                   | 50 3 1 2 100 4 2 1 24; Up to 8 possible for F-blocks  2 048  Yes   |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> <li>Number of startup OBs</li> <li>Number of asynchronous error OBs</li> <li>Number of synchronous error OBs</li> <li>Number of diagnostic alarm OBs</li> <li>Nesting depth</li> <li>per priority class</li> <li>Counters, timers and their retentivity</li> <li>S7 counter</li> <li>Number</li> <li>Retentivity</li> <li>— adjustable</li> <li>IEC counter</li> <li>Number</li> <li>Retentivity</li> <li>— adjustable</li> <li>S7 times</li> </ul>                 | 50 3 1 2 100 4 2 1 24; Up to 8 possible for F-blocks  2 048  Yes  Any (only limited by the main memory)  Yes |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> <li>Number of startup OBs</li> <li>Number of asynchronous error OBs</li> <li>Number of synchronous error OBs</li> <li>Number of diagnostic alarm OBs</li> <li>Nesting depth</li> <li>per priority class</li> <li>Counters, timers and their retentivity</li> <li>S7 counter</li> <li>Number</li> <li>Retentivity</li> <li>— adjustable</li> <li>IEC counter</li> <li>Number</li> <li>Retentivity</li> <li>— adjustable</li> <li>S7 times</li> <li>Number</li> </ul> | 50 3 1 2 100 4 2 1 1 24; Up to 8 possible for F-blocks  2 048  Yes  Any (only limited by the main memory)    |
| <ul> <li>Number of cyclic interrupt OBs</li> <li>Number of process alarm OBs</li> <li>Number of DPV1 alarm OBs</li> <li>Number of isochronous mode OBs</li> <li>Number of technology synchronous alarm OBs</li> <li>Number of startup OBs</li> <li>Number of asynchronous error OBs</li> <li>Number of synchronous error OBs</li> <li>Number of diagnostic alarm OBs</li> <li>Nesting depth</li> <li>per priority class</li> <li>Counters, timers and their retentivity</li> <li>S7 counter</li> <li>Number</li> <li>Retentivity</li> <li>— adjustable</li> <li>IEC counter</li> <li>Number</li> <li>Retentivity</li> <li>— adjustable</li> <li>S7 times</li> </ul>                 | 50 3 1 2 100 4 2 1 24; Up to 8 possible for F-blocks  2 048  Yes  Any (only limited by the main memory)  Yes |

| IEC timer   |  |
|---|--|
| Number  | Any (only limited by the main memory)  |
| Number     Retentivity  | Any (only limited by the main memory)  |
| •   | Van  |
| — adjustable  | Yes  |
| Data areas and their retentivity                                |  |
| Retentive data area (incl. timers, counters, flags), max.       | 410 kbyte; For storage in NVRAM; for storage in mass storage 5 242 020 bytes       |
| Flag  |  |
| • Size, max.  | 16 kbyte   |
| Number of clock memories  | 8; 8 clock memory bit, grouped into one clock memory byte                          |
| Data blocks   |  |
| <ul> <li>Retentivity adjustable</li> </ul>                      | Yes  |
| Retentivity preset  | No   |
| Local data  |  |
| <ul> <li>per priority class, max.</li> </ul>                    | 64 kbyte; max. 16 KB per block   |
| Address area  |  |
| Number of IO modules  | 8 192  |
| I/O address area  |  |
| • Inputs  | 32 kbyte; All inputs are in the process image                                      |
| Outputs   | 32 kbyte; All outputs are in the process image                                     |
| Subprocess images   | 22   |
| Number of subprocess images, max.                               | 32   |
| Hardware configuration  | 02   |
|   | V  |
| Integrated power supply   | Yes  |
| Number of distributed IO systems                                | 20   |
| Number of DP masters  |  |
| • Via CM  | 1  |
| Number of IO Controllers  |  |
| via PC interfaces   | 1  |
| Rack  |  |
| <ul> <li>Modules per rack, max.</li> </ul>                      | 64; CPU 1515SP PC + 64 modules + server module                                     |
| <ul> <li>Quantity of operable ET 200SP modules, max.</li> </ul> | 64   |
| <ul> <li>Quantity of operable ET 200AL modules, max.</li> </ul> | 16   |
| Number of lines, max.   | 1  |
| PtP CM  |  |
| Number of PtP CMs   | the number of connectable PtP CMs is only limited by the number of available slots |
| Time of day   |  |
| Clock   |  |
| • Type  | Hardware clock   |
| Hardware clock (real-time)                                      | Yes; Resolution: 1 s   |
| Backup time   | 6 wk; At 40 °C ambient temperature, typically                                      |
| Deviation per day, max.   | 10 s; Typ.: 2 s  |
| Clock synchronization   | . 71   |
| • supported   | Yes  |
| • to DP, master   | Yes  |
| on Ethernet via NTP   | Yes  |
| on Windows clock, slave   | Yes  |
| Interfaces  |  |
| Number of industrial Ethernet interfaces                        | 2  |
| Number of PROFINET interfaces                                   | 1  |
| Number of PROFIBUS interfaces                                   |  |
| Number of RS 485 interfaces                                     | 1; Via CM DP module  |
| Number of USB interfaces  | 4; 2x USB 2.0, 2x USB 3.0 on front side  |
| Number of SD card slots   | 1  |
| Video interfaces  |  |
|   | 1v DienlayPort   |
| Graphics interface  1. Interface                                | 1x DisplayPort   |
|   |  |

| Interface type  | PROFINET   |
|---|--|
| automatic detection of transmission rate  | Yes  |
| Autonegotiation   | Yes  |
| Autocrossing  | Yes  |
| Number of connections   | 88   |
| Interface types   |  |
| RJ 45 (Ethernet)  | Yes; Via BusAdapter BA 2x RJ45   |
| — Transmission rate, max.   | 100 Mbit/s   |
| <ul> <li>Industrial Ethernet status LED</li> </ul>  | Yes  |
| <ul> <li>Number of ports</li> </ul>   | 2  |
| • integrated switch   | Yes  |
| BusAdapter (PROFINET)   | Yes; Compatible BusAdapter: BA 2x RJ45, BA 2x FC, BA 2x SCRJ (from FS03, V2.2), BA SCRJ / RJ45 (from FS03, V3.1), BA SCRJ / FC (from FS03, V3.1), BA 2x LC (from FS03, V3.3), BA LC / RJ45 (from FS03, V3.3), BA LC / FC (from FS03, V3.3) |
| Protocols   |  |
| <ul> <li>PROFINET IO Controller</li> </ul>  | Yes  |
| PROFINET IO Device  | Yes  |
| <ul> <li>SIMATIC communication</li> </ul>   | Yes  |
| Open IE communication   | Yes  |
| Web server  | Yes  |
| PROFINET IO Controller  |  |
| Services  |  |
| — Isochronous mode  | Yes  |
| <ul> <li>shortest clock pulse</li> </ul>  | 500 μs   |
| — IRT   | Yes  |
| — PROFlenergy   | Yes  |
| — Prioritized startup   | Yes; max. 32 PROFINET devices; if you want to use the "Prioritized startup" functionality in STEP 7 for the PROFINET interface of the CPU, the CPU and the device must be separated by means of a switch (e.g. SCALANCE X205)              |
| <ul> <li>Number of connectable IO Devices, max.</li> </ul>                                  | 128  |
| <ul> <li>Of which IO devices with IRT, max.</li> </ul>                                      | 64   |
| — of which in line, max.  | 64   |
| <ul> <li>Number of connectable IO Devices for RT,<br/>max.</li> </ul>                       | 128  |
| — of which in line, max.  | 128  |
| Number of IO Devices that can be  | 8  |
| simultaneously activated/deactivated, max.  — IO Devices changing during operation (partner | Yes  |
| ports), supported   |  |
| <ul> <li>Number of IO Devices per tool, max.</li> </ul>                                     | 8  |
| — Updating times  | The minimum value of the update time also depends on communication share set for PROFINET IO, on the number of IO devices, and on the quantity of configured user data   |
| Update time for IRT   |  |
| — for send cycle of 500 μs  | 500 μs to 8 ms   |
| — for send cycle of 1 ms  | 1 ms to 16 ms  |
| — for send cycle of 2 ms  | 2 ms to 32 ms  |
| — for send cycle of 4 ms  | 4 ms to 64 ms  |
| <ul> <li>With IRT and parameterization of "odd" send cycles</li> </ul>                      | Update time = set "odd" send clock (any multiple of 125 $\mu s$ : 375 $\mu s$ , 625 $\mu s$ 3 875 $\mu s$ )  |
| Update time for RT  |  |
| — for send cycle of 500 μs  | 500 μs to 256 ms   |
| — for send cycle of 1 ms  | 1 ms to 512 ms   |
| — for send cycle of 2 ms  | 2 ms to 512 ms   |
| — for send cycle of 4 ms  | 4 ms to 512 ms   |
| — 101 3cHd Cydic 01 4 His   |  |
| Address area  |  |
|   | 8 kbyte  |

| Services  |  |
|---|--|
| <ul><li>— Isochronous mode</li></ul>                              | No   |
| <ul> <li>shortest clock pulse</li> </ul>                          | 500 μs   |
| — IRT   | Yes  |
| — PROFlenergy   | Yes  |
| <ul> <li>Prioritized startup</li> </ul>                           | Yes  |
| — Shared device   | Yes  |
| <ul> <li>Number of IO Controllers with shared device,</li> </ul>  | 4  |
| max.  |  |
| <ul> <li>Asset management record</li> </ul>                       | Yes  |
| 2. Interface  |  |
| Interface type  | Integrated Ethernet interface  |
| automatic detection of transmission rate                          | Yes  |
| Autonegotiation   | Yes  |
| Autocrossing  | Yes  |
| Interface types   |  |
| RJ 45 (Ethernet)  | Yes; Integrated  |
| — Transmission rate, max.   | 1 000 Mbit/s   |
| Industrial Ethernet status LED                                    | No   |
| Number of ports   | 1  |
|   |  |
| 3. Interface  | DDOCIDLIC with CM DD   |
| Interface type  | PROFIBUS with CM DP  |
| Number of connections via this interface                          | 44   |
| Interface types   |  |
| • RS 485  | Yes  |
| Protocols   |  |
| <ul> <li>PROFIBUS DP master</li> </ul>                            | Yes  |
| <ul> <li>PROFIBUS DP slave</li> </ul>                             | Yes  |
| SIMATIC communication   | Yes  |
| PROFIBUS DP master  |  |
| <ul> <li>Number of DP slaves, max.</li> </ul>                     | 125  |
| Services  |  |
| — Equidistance  | No   |
| — Isochronous mode  | No   |
| Address area  |  |
| — Inputs, max.  | 8 kbyte  |
| — Outputs, max.   | 8 kbyte  |
| Interface types   |  |
| RS 485  |  |
| Transmission rate, max.   | 12 Mbit/s  |
|   | 12 MIDIUS  |
| Protocols   |  |
| Number of connections   |  |
| Number of connections, max.                                       | 88   |
| <ul> <li>Number of connections reserved for ES/HMI/web</li> </ul> | 10   |
| Number of S7 routing paths  | 16   |
| Redundancy mode   |  |
| Media redundancy  |  |
| — MRP   | Yes  |
| — MRPD  | Yes  |
| <ul> <li>Switchover time on line break, typ.</li> </ul>           | 200 ms   |
| <ul> <li>Number of stations in the ring, max.</li> </ul>          | 50   |
| SIMATIC communication   |  |
| PG/OP communication   | Yes  |
| S7 routing  | Yes  |
| S7 communication, as server                                       | Yes  |
| S7 communication, as client                                       | Yes  |
| User data per job, max.   | 64 kbyte; BSEND/BRCV: 64 KB; PUT/GET: 960 bytes  |
|   | The first of the f |
| Open IE communication   |  |

| • TCP/IP  | Yes   |
|---|---|
| <ul><li>— Data length, max.</li></ul>   | 64 kbyte  |
| <ul><li>ISO-on-TCP (RFC1006)</li></ul>  | Yes   |
| <ul><li>— Data length, max.</li></ul>   | 64 kbyte  |
| • UDP   | Yes   |
| — Data length, max.   | 2 048 byte  |
| • SNMP  | Yes   |
| • DCP   | Yes   |
| • LLDP  | Yes   |
| Web server  |   |
| • HTTP  | Yes; Via Windows and PROFINET interface   |
| • HTTPS   | Yes; Via Windows and PROFINET interface   |
| OPC UA  |   |
| Runtime license required  | Yes; "Small" license required   |
| OPC UA Client   | Yes; From SW CPU 1505SP V2.6  |
| OPC UA Server   | Yes; Data access (read, write, subscribe), runtime license required   |
| — Application authentication  | Yes; Available security policies: None, Basic128Rsa15, Basic256Rsa15, Basic256Sha256  |
| — Security policies   | Yes; Available security policies: None, Basic128Rsa15, Basic256Rsa15, Basic256Sha256  |
| <ul> <li>User authentication</li> </ul>   | Yes; "anonymous" or by user name & password   |
| Further protocols   |   |
| • MODBUS  | Yes; MODBUS TCP   |
| S7 message functions  |   |
| Number of login stations for message functions, max.  | 32  |
| Program alarms  | Yes   |
| Number of configurable program messages, max.   | 10 000  |
| Number of simultaneously active program alarms  | 1 000   |
| Number of program alarms  | 1 000   |
| Number of alarms for system diagnostics   | 200   |
|   |   |
| <ul> <li>Number of alarms for motion technology objects</li> </ul>  | 160   |
| Number of alarms for motion technology objects  Test commissioning functions  | 160   |
| Test commissioning functions  |   |
| Test commissioning functions Joint commission (Team Engineering)  | Yes; Parallel online access possible for up to 8 engineering systems  |
| Test commissioning functions  Joint commission (Team Engineering)  Status block   | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously  |
| Test commissioning functions  Joint commission (Team Engineering)  Status block  Single step  | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No   |
| Test commissioning functions Joint commission (Team Engineering) Status block Single step Number of breakpoints   | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously  |
| Test commissioning functions Joint commission (Team Engineering) Status block Single step Number of breakpoints Status/control  | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8   |
| Test commissioning functions Joint commission (Team Engineering) Status block Single step Number of breakpoints Status/control  • Status/control variable   | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8 Yes   |
| Test commissioning functions  Joint commission (Team Engineering)  Status block  Single step  Number of breakpoints  Status/control  • Status/control variable  • Variables   | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8   |
| Test commissioning functions Joint commission (Team Engineering) Status block Single step Number of breakpoints Status/control  • Status/control variable • Variables • Number of variables, max.   | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters  |
| Test commissioning functions Joint commission (Team Engineering) Status block Single step Number of breakpoints Status/control  • Status/control variable • Variables • Number of variables, max. — of which status variables, max.   | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters  |
| Test commissioning functions Joint commission (Team Engineering) Status block Single step Number of breakpoints Status/control  • Status/control variable • Variables • Number of variables, max. — of which status variables, max. — of which control variables, max.  | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters  |
| Test commissioning functions Joint commission (Team Engineering) Status block Single step Number of breakpoints Status/control  • Status/control variable  • Variables  • Number of variables, max.  — of which status variables, max.  — of which control variables, max.  Forcing   | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters 200 200  |
| Test commissioning functions Joint commission (Team Engineering) Status block Single step Number of breakpoints Status/control  Status/control variable Variables Number of variables, max. — of which status variables, max. — of which control variables, max. Forcing Forcing  | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters 200 200  Yes   |
| Test commissioning functions Joint commission (Team Engineering) Status block Single step Number of breakpoints Status/control  Status/control  Status/control variable  Variables  Number of variables, max.  of which status variables, max.  of which control variables, max.  Forcing  Forcing  Forcing, variables  | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters 200 200  Yes Inputs, outputs   |
| Test commissioning functions Joint commission (Team Engineering) Status block Single step Number of breakpoints Status/control  Status/control variable  Variables  Number of variables, max.  of which status variables, max.  of which control variables, max.  Forcing  Forcing  Forcing  Forcing, variables, max.   | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters 200 200  Yes   |
| Test commissioning functions Joint commission (Team Engineering)  Status block Single step Number of breakpoints  Status/control  Status/control variable  Variables  Number of variables, max.  of which status variables, max.  of which control variables, max.  Forcing  Forcing  Forcing  Forcing, variables  Number of variables, max.  Diagnostic buffer   | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters  200 200  Yes Inputs, outputs 200  |
| Test commissioning functions Joint commission (Team Engineering)  Status block Single step Number of breakpoints Status/control  Status/control variable  Variables  Number of variables, max.  of which status variables, max.  of which control variables, max.  Forcing  Forcing  Forcing  Forcing, variables  Number of variables, max.  Diagnostic buffer  present   | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters  200 200  Yes Inputs, outputs 200  Yes   |
| Test commissioning functions Joint commission (Team Engineering) Status block Single step Number of breakpoints Status/control  Status/control  Status/control variable  Variables  Number of variables, max.  of which status variables, max.  of which control variables, max.  Forcing  Forcing  Forcing  Forcing, variables  Number of variables, max.  Diagnostic buffer  present  Number of entries, max.   | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters  200 200  Yes Inputs, outputs 200  Yes Inputs, outputs 200  Yes Inputs, outputs 200                |
| Test commissioning functions Joint commission (Team Engineering)  Status block Single step Number of breakpoints  Status/control  Status/control  Status/control variable  Variables  Number of variables, max.  of which status variables, max.  of which control variables, max.  Forcing  Forcing  Forcing  Forcing, variables  Number of variables, max.  Diagnostic buffer  present  Number of entries, max.  of which powerfail-proof   | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters  200 200  Yes Inputs, outputs 200  Yes   |
| Test commissioning functions Joint commission (Team Engineering)  Status block Single step Number of breakpoints Status/control  Status/control  Status/control variable  Variables  Number of variables, max.  of which status variables, max.  of which control variables, max.  Forcing  Forcing  Forcing, variables  Number of variables, max.  Diagnostic buffer  present  Number of entries, max.  of which powerfail-proof   | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters  200 200  Yes Inputs, outputs 200  Yes Inputs, outputs 200  Yes Inputs, outputs 200  Yes 1 000 300 |
| Test commissioning functions Joint commission (Team Engineering)  Status block Single step Number of breakpoints  Status/control  Status/control variable  Variables  Number of variables, max.  of which status variables, max.  of which control variables, max.  Forcing  Forcing  Forcing, variables  Number of variables, max.  Diagnostic buffer  present  Number of entries, max.  of which powerfail-proof  Traces  Number of configurable Traces   | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters  200 200  Yes Inputs, outputs 200  Yes Inputs, outputs 200  Yes 1 000 300                          |
| Test commissioning functions Joint commission (Team Engineering)  Status block Single step Number of breakpoints  Status/control  Status/control variable  Variables  Number of variables, max.  of which status variables, max.  forcing  Forcing  Forcing, variables  Number of variables, max.  Diagnostic buffer  present  Number of entries, max.  of which powerfail-proof  Traces  Number of configurable Traces  Memory size per trace, max.  | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters  200 200  Yes Inputs, outputs 200  Yes Inputs, outputs 200  Yes Inputs, outputs 200  Yes 1 000 300 |
| Test commissioning functions Joint commission (Team Engineering)  Status block Single step Number of breakpoints  Status/control  Status/control  Status/control variable  Variables  Number of variables, max.  of which status variables, max.  of which control variables, max.  Forcing  Forcing  Forcing, variables  Number of variables, max.  Diagnostic buffer  present  Number of entries, max.  of which powerfail-proof  Traces  Number of configurable Traces  Memory size per trace, max.  Interrupts/diagnostics/status information   | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters  200 200  Yes Inputs, outputs 200  Yes Inputs, outputs 200  Yes 1 000 300                          |
| Test commission (Team Engineering)  Status block Single step Number of breakpoints Status/control  Status/control  Status/control variable  Variables  Number of variables, max.  of which status variables, max.  of which control variables, max.  Forcing  Forcing  Forcing, variables  Number of variables, max.  Diagnostic buffer  present  Number of entries, max.  of which powerfail-proof  Traces  Number of configurable Traces  Memory size per trace, max.  Interrupts/diagnostics/status information  Diagnostics indication LED  | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters  200 200  Yes Inputs, outputs 200  Yes 1 000 300  4 512 kbyte                                      |
| Test commission (Team Engineering)  Status block Single step Number of breakpoints Status/control  Status/control  Status/control variable  Variables  Number of variables, max.  of which status variables, max.  forcing  Forcing  Forcing, variables  Number of variables, max.  Interrupts/diagnostics/status information  Find the process of the process | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters  200 200  Yes Inputs, outputs 200  Yes 1 000 300  4 512 kbyte                                      |
| Test commission (Team Engineering)  Status block Single step Number of breakpoints Status/control  Status/control  Status/control variable  Variables  Number of variables, max.  of which status variables, max.  of which control variables, max.  Forcing  Forcing  Forcing, variables  Number of variables, max.  Diagnostic buffer  present  Number of entries, max.  of which powerfail-proof  Traces  Number of configurable Traces  Memory size per trace, max.  Interrupts/diagnostics/status information  Diagnostics indication LED  | Yes; Parallel online access possible for up to 8 engineering systems Yes; up to 8 simultaneously No 8  Yes Inputs, outputs, memory bits, DB, times, counters  200 200  Yes Inputs, outputs 200  Yes 1 000 300  4 512 kbyte                                      |

| Supported technology objects  |  |
|---|--|
| Motion Control  | Yes  |
| <ul> <li>Number of available Motion Control resources for</li> </ul>  | 2 400  |
| technology objects  |  |
| <ul> <li>Required Motion Control resources</li> </ul>   |  |
| <ul> <li>per speed-controlled axis</li> </ul>   | 40; per axis   |
| per positioning axis  | 80; per axis   |
| — per synchronous axis  | 160; per axis  |
| — per external encoder  | 80; per external encoder   |
| — per output cam  | 20; per cam  |
| — per cam track   | 160; per cam track   |
| ·   | 40; per can track  |
| — per probe   | 40, per probe  |
| Positioning axis  | 45   |
| Number of positioning axes at motion control cycle of 4 ms (typical value)  | 15   |
| Number of positioning axes at motion control cycle of 8 ms (typical value)  | 30   |
| Controller  |  |
| <ul><li>PID_Compact</li></ul>   | Yes; Universal PID controller with integrated optimization                           |
| PID_3Step   | Yes; PID controller with integrated optimization for valves                          |
| PID-Temp  | Yes; PID controller with integrated optimization for temperature                     |
| Counting and measuring  |  |
| High-speed counter  | Yes  |
| Standards, approvals, certificates  |  |
| CE mark   | Yes  |
| CSA approval  | Yes  |
| cULus   | Yes  |
| FM approval   | Yes  |
| RCM (formerly C-TICK)   | Yes  |
| Highest safety class achievable in safety mode  | 100  |
| Performance level according to ISO 13849-1  | PLe  |
| SIL acc. to IEC 61508   | SIL 3  |
|   |  |
| Probability of failure (for service life of 20 years and repa<br>— Low demand mode: PFDavg in accordance  | < 2.00E-05   |
| with SIL3  — High demand/continuous mode: PFH in  | < 1.00E-09 1/h   |
| accordance with SIL3  |  |
| Ambient conditions  |  |
| Ambient temperature during operation  |  |
| • min.  | -20 °C   |
| • max.  | Up to 60 °C with max. 32 ET 200SP modules; up to 55 °C with max. 64 ET 200SP modules |
| <ul> <li>horizontal installation, min.</li> </ul>   | -20 °C   |
| <ul> <li>horizontal installation, max.</li> </ul>   | 60 °C  |
| <ul> <li>vertical installation, min.</li> </ul>   | -20 °C   |
| <ul> <li>vertical installation, max.</li> </ul>   | 50 °C; With max. 32 ET 200SP modules   |
| Ambient temperature during storage/transportation   |  |
| min.  | -40 °C   |
| • max.  | 70 °C  |
| Vibrations  |  |
|   | Yes  |
| <ul> <li>Operation, tested according to IFC 60068-2-6</li> </ul>  |  |
| <ul> <li>Operation, tested according to IEC 60068-2-6</li> <li>Transport tested acc. to IEC 60068-2-6</li> </ul>  | Yes  |
| Transport, tested acc. to IEC 60068-2-6   | Yes  |
| Transport, tested acc. to IEC 60068-2-6  Shock testing  |  |
| Transport, tested acc. to IEC 60068-2-6  Shock testing tested according to IEC 60068-2-6  | Yes  |
| Transport, tested acc. to IEC 60068-2-6  Shock testing  tested according to IEC 60068-2-6  tested according to IEC 60068-2-27                                     | Yes<br>Yes   |
| Transport, tested acc. to IEC 60068-2-6  Shock testing  tested according to IEC 60068-2-6  tested according to IEC 60068-2-27  tested according to IEC 60068-2-29 | Yes<br>Yes<br>Yes  |
| Transport, tested acc. to IEC 60068-2-6  Shock testing  tested according to IEC 60068-2-6  tested according to IEC 60068-2-27                                     | Yes<br>Yes   |

| Configuration   |  |  |
|---|--|--|
| Programming   |  |  |
| Programming language  |  |  |
| — LAD   | Yes; incl. failsafe                    |  |
| — FBD   | Yes; incl. failsafe                    |  |
| — STL   | Yes                                    |  |
| — SCL   | Yes                                    |  |
| — CFC   | No                                     |  |
| — GRAPH   | Yes                                    |  |
| Know-how protection   |  |  |
| <ul> <li>User program protection/password protection</li> </ul> | Yes                                    |  |
| <ul> <li>Copy protection</li> </ul>                             | Yes                                    |  |
| Block protection  | Yes                                    |  |
| Access protection   |  |  |
| <ul> <li>Protection level: Write protection</li> </ul>          | Yes                                    |  |
| <ul> <li>Protection level: Read/write protection</li> </ul>     | Yes                                    |  |
| Protection level: Complete protection                           | Yes                                    |  |
| Cycle time monitoring   |  |  |
| <ul> <li>lower limit</li> </ul>                                 | adjustable minimum cycle time          |  |
| upper limit   | adjustable maximum cycle time          |  |
| Open Development interfaces                                     |  |  |
| Size of ODK SO file, max.                                       | 5.8 Mbyte                              |  |
| Peripherals/Options   |  |  |
| SD card   | Optionally for additional mass storage |  |
| Dimensions  |  |  |
| Width   | 160 mm                                 |  |
| Height  | 117 mm                                 |  |
| Depth   | 75 mm                                  |  |
| Weights   |  |  |
| Weight, approx.   | 0.83 kg                                |  |
| last modified:  | 3/2/2021 🗗                             |  |